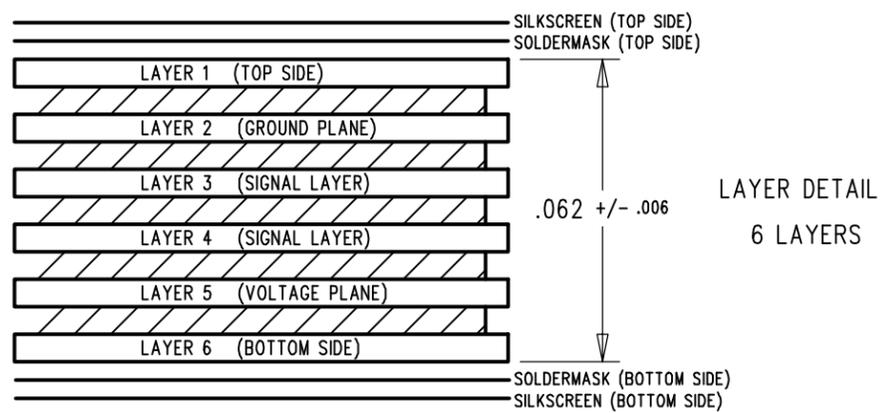


- NOTES : (UNLESS SPECIFIED OTHERWISE.)
- 1) ALL DIMENSIONS ARE IN INCHES.TOLERANCE IS +/- 0.005 CONFORMING TO IPC-A-600. GIVEN MECHANICAL DIMENSIONS ARE IN MILLIMETRES.
 - 2) HOLE SIZES ARE SPECIFIED IN THOUSANDTHS. HOLE SIZES APPLY AFTER PLATING. HOLE SIZE TOLERANCE TO BE ± 0.003, UNLESS SPECIFIED OTHERWISE. PLATING TO BE 0.0010 MINIMUM.
 - 3) MATERIAL : FR-4-2 NATURAL EPOXY/FIBERGLASS
0.5 OZ. COPPER ALL LAYERS.
2 SIDES PLATED.
FINISHED THICKNESS : $0.062 \pm \begin{matrix} +0.006 \\ -0.006 \end{matrix}$
 - 4) APPLY SOLDERMASK OVER BARE COPPER, BOTH SIDES. FINISH ALL EXPOSED COPPER SURFACES WITH 60/40% TIN/LEAD SOLDER, 100u" TO 1000u".
 - 5) THE LOCATION OF THE MARKING SHOULD BE ON THE SURFACE OF THE PCB, NOT THE EDGE.
 - 6) APPLY SOLDERMASK TO COMPONENT SIDE AND SOLDER SIDE, USING
 PC401 OR EQUIVALENT.
 LIQUID PHOTO IMAGEABLE.
 DRY FILM.
COLOR : GREEN.
USE SEPARATE FILM FOR COMPONENT SIDE AND SOLDER SIDE SOLDERMASKS. FABRICATOR SHALL MAKE NECESSARY MODIFICATIONS TO SOLDERMASK PLOTFILES FOR OPTIMAL SOLDERMASK COVERAGE BETWEEN FINE PITCH COMPONENT LEADS.
 - 7) APPLY LEGEND TO COMPONENT SIDE USING NON-CONDUCTIVE EPOXY INK. COLOR : WHITE. FABRICATOR SHALL MAKE NECESSARY MODIFICATIONS TO LEGEND PLOTFILES TO ENSURE NO LEGEND INK COVERS ANY COMPONENT PAD OR VIA PAD.
 - 8) PCB SHALL BE CLEAN AND FREE FROM DIRT, OIL, FINGERPRINTS, CORROSION, AND ANY OTHER FOREIGN MATERIAL.
 - 9) MODIFIED PLOTFILES ARE TO BE RETURNED BEFORE ORDER DELIVERED.
 - 10) FABRICATOR HAS APPROVAL TO REMOVE NON-FUNCTIONAL PADS FROM INNER LAYERS.
 - 11) BOW AND TWIST SHALL BE LESS THAN 0.005" PER INCH.
 - 12) ALL PRINTED CIRCUIT BOARD NETS SHALL BE 100% ELECTRICALLY TESTED FOR OPENS AND SHORTS. MARK TEST VERIFICATION STAMP APPROXIMATELY WHERE SHOWN, ALONG EDGE OF PCB, USING WHITE INK.
 - 13) FABRICATION OF P.W.B. TO COMPLY WITH IPC-A-600, CLASS II , CURRENT REVISION.



ZF LINUX DEVICES, INC. DATE : 11/6/2000 9400-0050-00

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APPROVALS		DATE	ZF Linux Devices, Inc. 1052 Elwell Court Palo Alto, CA 94303 (650) 965-3800			
DRAWN: Eili Puusepp		11/6/2000	LD NDD Assembly Drawing Bottom			
CHECKED: Kaido Kevvai		11/6/2000				
PROJ MGR: ARTEC DG			DRAWING NUMBER		SCALE	SHEET
CLIENT: ZF Linux Devices, Inc.		SIZE B	9400-0050-00-X4		1 : 1	2 OF 2
					RELEASE 01	REVISION 4